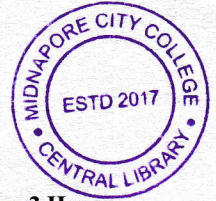


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PG CBCS
M.Sc. Semester-IV Examination, 2022
PHYSICS
PAPER: PHS495B
(APPLIED ELECTRONICS PRACTICAL)



Full Marks: 50

Time: 3 Hours

(Experiment: 35, Viva Voce: 10, Note Book: 5)

Everyone will do one experiment amongst the following. The marks distribution will be as follows: Theory with working formula (5), measurements and data tabulation (12), Graph plot and Calculation (10), error analysis (3), result and discussion (5).

1. Design a Schmitt trigger circuit using μA 741 OP-AMP and plot the hysteresis curve and determine the immunity window.
2. Design a Digital to Analog Converter (DAC) using R-2R Ladder Circuit and compare its theoretical and measured values.
3. Design a Pulse Amplitude Modulation Circuit and Demonstrate its modulation, demodulation, transmission and reception performance.
4. Design a Pulse Width Modulation Circuit using 555 timer IC and Demonstrate its modulation, demodulation, transmission and reception performance.
5. Design a VCO using NE566 and study its performance.
6. Demonstrate addition and subtraction of two binary number using 8085 Microprocessor.
