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PG CBCS
M.SC. Semester-III Examination, 2021
PHYSICS
PAPER: PHS 396B (SPL PRACTICAL)
(APPLIED ELECTRONICS I)

Full Marks: 50

Time: 3 Hours

Answer any TWO questions of the following:

2 X 25=50

1. Describe the experimental procedure of Logarithmic amplifier using a OP-AMP μA 741, diode and transistor by explaining theory, clear circuit diagram, range of input values, precautions etc.
2. Describe the experimental procedure of Anti-Logarithmic amplifier using a OP-AMP μA 741, diode and transistor by explaining theory, clear circuit diagram, precautions etc.
3. Describe the experimental procedure of active band filter using single stage μA 741 OP-AMP by explaining theory, clear circuit diagram, precautions etc.
4. Describe the experimental procedure of Shift registers (PISO, SISO, PIPO, SIPO) by explaining operational principles and clear circuit diagram.
5. Describe the experimental procedure of BCD addition and subtraction using Full adder IC by explaining theory, clear circuit diagram and operational principles.
6. Describe the experimental procedure of Multiplexer by explaining theory, formation, cascading and equation solving capabilities.
